

## PART B - FEE(S) TRANSMITTAL

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25944

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11/12/2003

OLIFF & BERRIDGE, PLC  
P.O. BOX 19928  
ALEXANDRIA, VA 22320



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(Depositor's name)

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/052,255	01/23/2002	Akihiko Ebina	111778	1930

**TITLE OF INVENTION:** METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY DEVICES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	02/12/2004
EXAMINER	ART UNIT		CLASS-SUBCLASS		
GEYER, SCOTT B	2829		438-257000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Oliff & Berridge, PLC

2 \_\_\_\_\_

3 \_\_\_\_\_

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

SEIKO EPSON CORPORATION

HALO LSI DESIGN & DEVICE TECHNOLOGY, INC.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TOKYO, JAPAN

WAPPINGER FALLS, NEW YORK

Please check the appropriate assignee category or categories (will not be printed on the patent):  individual  corporation or other private group entity  government

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Ian R. Vallejo

(Date)

Ian R. Vallejo, Reg. NO. 53,714 January 15, 2004

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01/16/2004 MGEBREM2 00000109 10052255

01 FC:1501

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